

FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

MAY 20 2002

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Attorney Docket Number

47139/JEJ/B600

Application Number

10/045,194

Filing Date

October 19, 2001

Applicant(s)

Sherman Chen, et al.

Group Art Unit

2631

Examiner Name

To be assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - kind code ² . (If known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
B.		5,262,854	11-16-1993	Ng
B.		5,307,177	04-26-1994	Shibata et al.
B.		5,396,567	03-07-1995	Jass
B.		5,614,952	03-25-1997	Boyce et al.
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B.		5,844,608	12-01-1998	Yu et al.

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
B.		VETRO, ANTHONY, et al., "Minimum Drift Architectures for 3-Layer Scalable DTV Decoding," IEEE Transactions on Consumer Electronics, Pages 527-536, Vol. 44, No. 3, August 1998.
B.		MOKRY, ROBERT, et al., "Minimal Error Drift in Frequency Scalability for Motion-Compensated DCT Coding," IEEE Transactions on Circuits and Systems for Video Technology, Pages 392-406, Vol. 4, No. 4, August 1994.

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B		YU, HAOPING, et al., "Block-Based Image Processor for Memory Efficient MPEG Video Decoding," 1999 IEEE International Conference on Consumer Electronics, Pages 114-115, Los Angeles, 1999.
B		BAO, JAY, et al., "HDTV Down-Conversion Decoder," IEEE Transactions on Consumer Electronics, Pages 402-410, Vol. 42, No. 3, August 1996.
B		SUN, HUIFANG, et al., "A New Approach for Memory Efficient ATV Decoding," 1997 IEEE International Conference on Consumer Electronics, Pages 174-175, Los Angeles, 1997.
B		LEE, Dong-Ho, et al., "HDTV Video Decoder Which can be Implemented With Low Complexity," IEEE International Conference on Consumer Electronics, Pages 6-7, 1994.
B		SUN, HUIFANG, "Hierarchical Decoder for MPEG Compressed Video Data," IEEE Transactions on Consumer Electronics, Pages 559-564, Vol. 39, No. 3, August 1993.
B		GERSHO, ALLEN, et al., "The Lloyd Quantizer Design Algorithm," Vector Quantization and Signal Compression (The Kluwer International Series in Engineering and Computer Science), Chapter 6: Scaler Quantization II, Pages 188-190, Kluwer Academic Publishers, Boston/Dordrecht/London, January 1992.

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